

# AWS Cloud

Reduce semiconductor design costs and accelerate time to market

As semiconductor chip design continues to evolve, including the proliferation of advanced nodes and more complex design processes, organizations are starting to consider how they can provide shorter design cycles and tap new tools and methodologies to improve existing processes.

Using HCLTech's years of experience in leading-edge silicon designs, semiconductor organizations can increase speed of innovation, gain more on-demand compute capacity, and enhance flexibility. By adding elastic cloud resources from Amazon Web Services (AWS), organizations can augment on-premises Electronic Design Automation (EDA) processes and/or create hybrid EDA infrastructures thereby reducing Total Cost of Ownership (TCO) and accelerating time to market.

## DIFFERENTIATORS

### TECHNICAL EXPERIENCE

- » 25+ years experience in semiconductor domain
- » Broad experience in cloud transformation services, migration readiness, and cloud architectures across multiple domains
- » Strong credentials and solutions for Hybrid/Cloud native services

### DELIVERY CREDENTIALS

- » Experience in designing and managing complex designs using leading EDA tools in variety of foundry nodes from 3nm to legacy
- » Extensive portfolio of solution accelerators, IPs, and best practices
- » Strong focus on design automation, CAD methodologies and DA support in managed services model

### GLOBAL FOOTPRINT

- » Global engineering team focused on semiconductor segment
- » Ability to scale up teams across the globe
- » Ability to support customers leveraging AWS global infrastructure and services

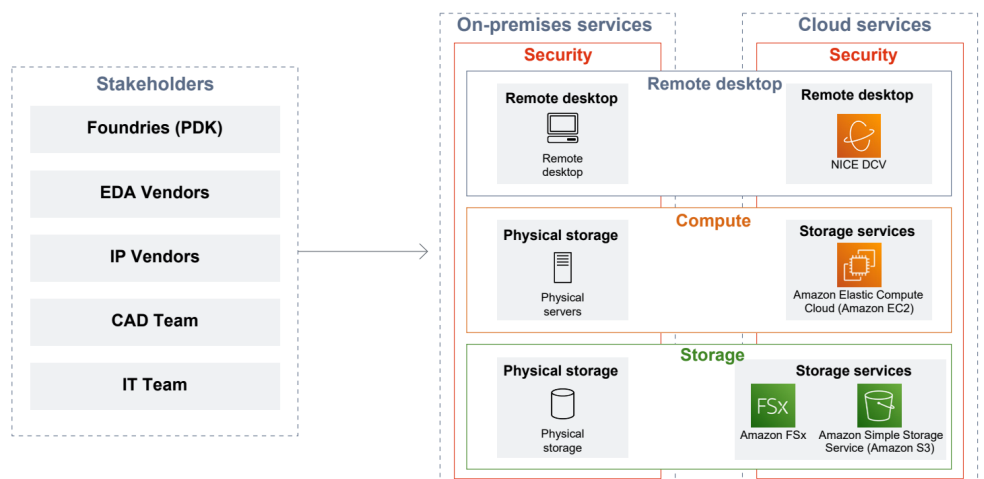
### DESIGN PARTNERSHIP ECOSYSTEM

- » Strong partnership with multiple foundries, EDA and IP vendors
- » TSMC DCA Member
- » GF Design Enablement Network Partner
- » ARM Approved Design Partner
- » Data Ownership
- » Cost optimization through right sized resources

## Augment on-premises data center services with AWS

Partnering with HCLTech – who understands the depths and nuances of the full chip design cycle – is a low-risk approach to accessing on-demand compute power on the cloud. Cloud EDA deployments are most effective when you work with an organization that can help you address the challenges that emerge when multiple ecosystem stakeholders are involved.

The diagram shows how on-premises processes and workflows can be augmented using the cloud providing a seamless, risk-free user experience.



# How HCLTech helps simplify your journey to the cloud

## Deployment, migration, and cloud insights on AWS

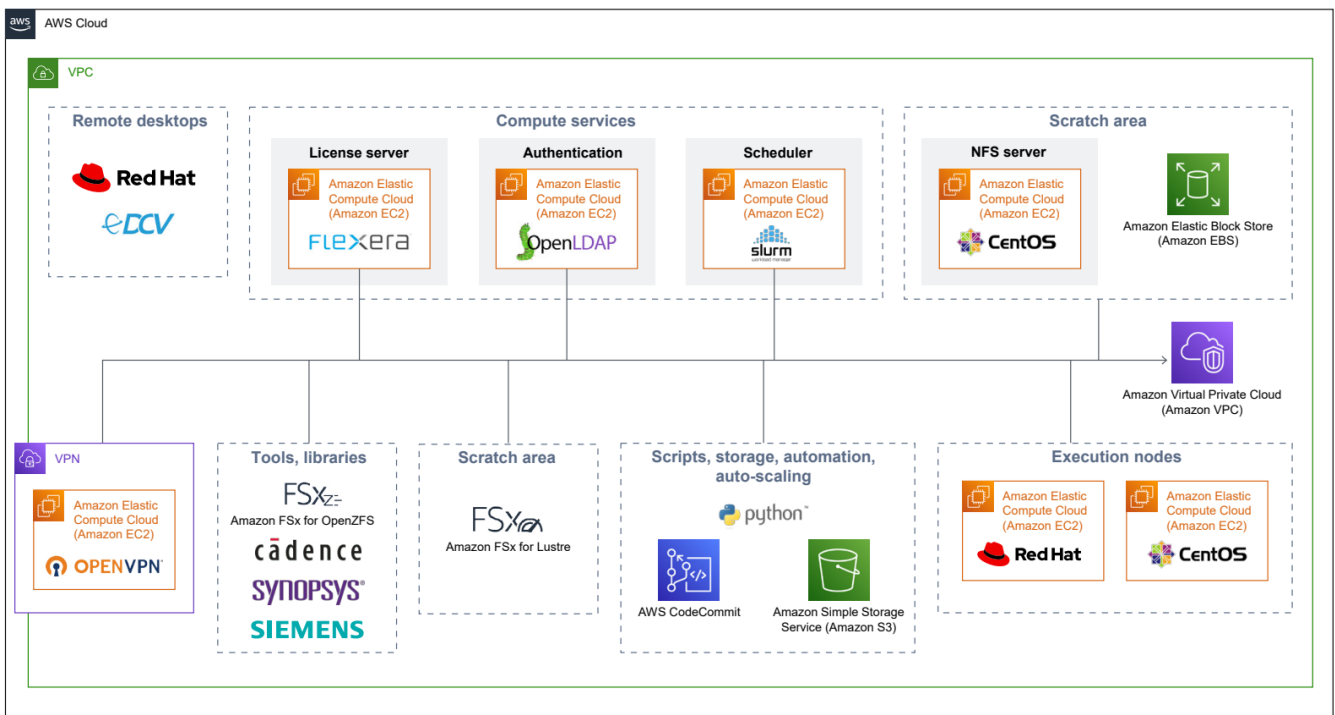
**Consulting services** include migration and readiness assessment, design consultancy, platforms and technology selection, EDA workloads architecture blueprint, and an operating model strategy.

**Build and transformation services** comprise EDA workload modernization, performance engineering, security and vulnerability testing, and data migration guidance.

**Operations management** includes 24x7 infrastructure and application monitoring, AIOps, release and incident management, security monitoring and management, and capacity management focused on scale and fault tolerance.

## Chip design workloads on AWS

The diagram below shows the end-to-end EDA environment designed to run different types of workloads. Using a combination of AWS best practices, solutions, and HCLTech domain experience on semiconductor design environments, HCLTech tests and builds different scenarios that help simplify your migration of EDA workloads to the cloud.



## Benefits of chip design workloads on AWS

### Deployment, migration, and cloud insights on AWS

Running EDA and related workloads on AWS allows chip designers and verification engineers to solve complex, compute-intensive issues. Elastic cloud services help reduce time-to-results and increase speed-to-market by scaling to larger numbers of parallel tasks that would not be possible in most on-premises environments. Using the AWS Cloud with HCLTech helps customers accelerate innovation, move products into the foundry pipeline faster, and enable rapid chip development.

## Get started

Contact us at [CloudEDA@hcl.com](mailto:CloudEDA@hcl.com) to learn how you can reduce semiconductor design risks using the AWS Cloud with HCLTech.

**HCLTech** | Supercharging Progress™

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